



PATENT ABSTRACTS OF JAPAN

(11)Publication number: 10209932 A

(43)Date of publication of application: 07.08.98

(51)Int. Cl. H04B 7/06
H04B 1/48

(21)Application number: 09010010

(22)Date of filing: 23.01.97

(71)Applicant: SAITAMA NIPPON DENKI KK

(72)Inventor: FUJITA NOBUYUKI

(54)TRANSMITTING DIVERSITY CIRCUIT FOR RADIO
DEVICE OF TDMA SYSTEM

needed.

COPYRIGHT: (C)1998,JPO

(57)Abstract

PROBLEM TO BE SOLVED: To eliminate the loss caused by a transmitting diversity synthesizer and to reduce the power consumption by deciding a priority according to the receiving field intensity levels and selecting in sequence the output antennas based on the decided priority to output the transmitting signals received from the corresponding transmitting circuit.

SOLUTION: The IF amplifiers IC 122 and 126 produce the level signals RSSI1 and RSSI2 which show the receiving signal levels based on the signals received via a 1st antenna 117. The level signals RSSI3 and RSSI4 are produced from the signals received via a 2nd antenna 133. A decision circuit 143 controls the switches 113, 114, 129 and 130 in the transmission timing of the next transmitting slot based on the results of comparison of level signals RSSI1 to RSSI4. Then the circuit 143 distributes the transmitting signals 1 and 2 to both antennas 117 and 133. The signals 1 and 2 are not simultaneously outputted from the same antenna and accordingly a transmitting diversity synthesizer is not

